REMARKS

In the patent application, claims 1-31 are pending. In the office action, all pending claims are rejected.

At section 3 of the office action, the Examiner states that the title of the invention is not descriptive. Applicant has changed the title to "Method and Device for Increasing Data Transfer in Multi-Media Card". Applicant believes that the new title adequately describes the invention where one or more unused data lines in a certain data transfer mode between a host device and a memory card are also used for data transfer.

At section 5, claims 1-31 are rejected under 35 U.S.C. 102(e) as being anticipated by *Moro* (U.S. Publication No. 2003/0056050). The Examiner states that *Moro* discloses a method for enhancing performance of a memory card by operating SPI or SD-1 mode so that there are (N-M) unused data lines, and generating a further signal so as to cause data to be exchanged using at least one of the unused data lines (Figure 6, paragraphs 0055-0059, Figure 6).

It is respectfully submitted that *Moro* discloses using one host device to exchange data with two SD cards. Figure 6 shows the operation in SD mode and Figure 7 shows the operation in SPI mode.

In paragraph 0056, *Moro* discloses that when SD 4-bit mode is used, all four data lines DAT3 to DAT0 are used for transferring data. No unused data lines are available. Thus, this mode has nothing to do with the claimed invention.

When SD 1-bit mode is used, only DAT0 is used for data transfer. DAT1 and DAT2 are not used. DAT3 is used for asynchronous interruption. However, no data is exchanged on the unused DAT1 or DAT2 line.

When SPI mode is used, only DAT0 is used for data transfer. DAT1 and DAT2 are not used. DAT3 is used for transmission of chip-set select. Again, no data is exchanged through the unused DAT1 or DAT2 line.

In the claimed invention, data is caused to exchange on at least one of the unused data lines due to the generation of at least one further signal when the host module is operated in the data mode that has unused data lines.
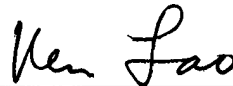
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In paragraph 0057, *Moro* describes using one host device to exchange data with two SD cards in an SD mode. In paragraph 0058, *Moro* describes using one host device to exchange data with two SD cards in the SPI mode. These paragraphs are irrelevant to the claimed invention.

In sum, *Moro* does <u>not</u> disclose using the <u>unused data lines</u> for additional data exchange in SD 1-bit mode and in SPI mode when a further signal is provided to the memory device. For the above reasons, the claimed invention is clearly distinguishable over the cited *Moro* reference.

CONCLUSION

Claims 1-31 are allowable. Early allowance of all pending claims is earnestly solicited.

Respectfully submitted,

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